



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of TI-36210  
Rakshit Kumar, et al. Art Unit: 2816  
Serial No.: 10/709,234 Examiner:  
Filed: April 22, 2004 Conf. No: 3233  
For: Memory Access System Providing Increased Throughput Rates When Accessing  
Large Volumes of Data

**LETTER TO THE OFFICIAL DRAFTSPERSON**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)**  
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on July 13, 2004.

  
Robin E. Barnum

Sir:

Please find enclosed the formal drawings for the subject case. Charge any necessary fees to Texas Instruments Incorporated, Deposit Account No. 20-0668. **This form is submitted in triplicate.**

Respectfully submitted,



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